

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	533	"precise exception"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:10
L3	20	"precise exception" and Preserv\$3 near3 (behavior or state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:11
L4	405	"precise exception" and cause and (solution or recover\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:12
L5	370	"precise exception" and cause and (solution or recover\$3) and (optimiz\$5 or optimis\$5 or reorder\$3 or reschedul\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:13
L6	351	"precise exception" and cause and (solution or recover\$3) and (optimiz\$5 or optimis\$5 or reorder\$3 or reschedul\$3) and parallel\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:25
L7	0	"precise exception" and cause and (solution or recover\$3) and (optimiz\$5 or optimis\$5 or reorder\$3 or reschedul\$3) and parallel\$3 and (log\$4 or record\$3 or journal\$3) near3 ((operations or methods or calls or procedures or functions) and addreses)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:26
L8	432	717/136.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:27
L9	22	717/136.ccls. and "binary translation"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:27
S1	94	exception and rollback and profil\$3 and (repeat\$3 or re-execut\$3 or re-occur\$4 or recover\$3) and parallel\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/07 16:10

S2	2	"5832205".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/07 16:10
S3	0	("exceptionandrollbackandprofil\$3 and(repeat\$3orre-execut\$3orre-oc cur\$4)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 17:28
S4	120	exception and rollback and profil\$3 and (repeat\$3 or re-execut\$3 or re-occur\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 07:18
S5	124	exception and rollback and profil\$3 and (repeat\$3 or re-execut\$3 or re-occur\$4 or recovery)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 07:23
S6	92	exception and rollback and profil\$3 and (repeat\$3 or re-execut\$3 or re-occur\$4 or recovery) and parallel\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 07:24
S7	94	exception and rollback and profil\$3 and (repeat\$3 or re-execut\$3 or re-occur\$4 or recover\$3) and parallel\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 09:03
S8	65	exception and rollback and profil\$3 and (repeat\$3 or re-execut\$3 or re-occur\$4 or recover\$3) and parallel\$2 and 7??/???.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:06
S9	11	("5099436" "5745693" "5752159" "5768614" "6138121" "6243838" "6249755" "6314533" "6438618" "6446136" "6496872").PN.	USPAT	OR	OFF	2004/07/06 09:42
S10	4	("5546455" "5740423" "5751798" "5828845").PN.	USPAT	OR	OFF	2004/07/06 09:45

S11	125	("4491946" "4713806" "4809160" "4823122" "4939638" "4956773" "5088036" "5109486" "5187787" "5218699" "5257369" "5293614" "5297283" "5307490" "5311591" "5339435" "5386568" "5390328" "5423042" "5440744" "5448740" "5452459" "5455952" "5471629" "5475792" "5475817" "5481721" "5504921" "5511197" "5524244" "5553282" "5555367" "5555427" "5557798" "5560003" "5561785" "5577231" "5594921" "5603031" "5617537" "5628005" "5640564" "5644768" "5649186" "5652888" "5655148" "5659751" "5671225" "5675796" "5680573" "5680617" "5684955" "5689709" "5706435" "5706502" "5724588" "5727145" "5737607" "5745678" "5745695" "5745703" "5745755" "5748897" "5754849" "5757925" "5761656" "5764897" "5768532" "5774551" "5778187" "5778228" "5778368" "5787425" "5787431" "5790548" "5802367" "5808911" "5809507" "5813013" "5815149" "5815709" "5815711" "5818448" "5829022" "5832219" "5832529" "5832593" "5835737" "5842018" "5844553" "5845129" "5860004" "5860153" "5864862" "5864866" "5872928" "5872973" "5875335" "5878411" "5884024" "5884079" "5887134" "5890158" "5892904" "5933497" "5935249" "5940827" "5944793" "5946485" "5946694" "5966531" "5969967" "5987506" "5999179" "6009782" "6008103" "6016496" "6016500" "6026414" "6031077"	USPAT	OR	OFF	2004/07/06 09:47
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S12	20	("4930071" "5295256" "5418717" "5542078" "5627979" "5630131" "5652884" "5809505" "5832498" "5857191" "5857197" "5893108" "5956725" "5970490" "5999734" "6038565" "6061515" "6175837" "6374256" "6453312").PN.	USPAT	OR	OFF	2004/07/06 09:52
S13	12	("4896257" "4954942" "5142672" "5442766" "5465337" "5526510" "5553255" "5561814" "5564111" "5566298" "5581722" "5613083").PN.	USPAT	OR	OFF	2004/07/06 09:57
S14	4	("5796954" "5812844" "5913925" "5968167").PN.	USPAT	OR	OFF	2004/07/06 10:03
S15	4	("5740423" "5751798" "5828845" "5878126").PN.	USPAT	OR	OFF	2004/07/06 10:05
S16	169	717/129.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:07
S17	224	717/130.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:07
S18	229	717/136.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:07
S19	250	717/151.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:07
S20	211	717/158.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:08
S21	728	712/23.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:08

S22	219	712/24.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:08
S23	105	712/203.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:08
S24	196	712/212.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:09
S25	448	712/228.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:09
S26	86	712/230.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:09
S27	152	712/235.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:09
S28	233	712/237.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:09
S29	439	712/244.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:09
S30	770	714/15.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:09
S31	69	714/17.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:10

S32	232	714/20.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:10
S33	200	714/35.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:10
S34	686	714/48.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:10
S35	232	714/49.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:10
S36	121	714/702.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:10
S37	97	714/707.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:10
S38	159	714/747.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:11
S39	718	determin\$3 adj cause near3 (exception or fault or event or interrupt or trap)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:12
S40	684	determin\$3 adj cause near2 (exception or fault or event or interrupt or trap)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:12
S41	110	determin\$3 adj cause near2 exception	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:12

S42	381	determin\$3 adj cause near2 (exception or fault or event or interrupt or trap) and ???/???.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:12
S43	91	determin\$3 adj cause near2 exception and ???/???.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/06 10:43
S44	1	"6341324".URPN.	USPAT	OR	OFF	2004/07/06 10:23
S45	4	("5148544" "5193189" "5295265" "5530804").PN.	USPAT	OR	OFF	2004/07/06 10:23
S46	2	"6247172".URPN.	USPAT	OR	OFF	2004/07/06 10:29
S47	18	("5974524" "6154877" "6345351" "6425118" "6594821" "6631514" "6795966" "6820255" "6826682"). pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/17 18:19
S48	2	("6496847").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/17 18:19
S49	25	("6463584" "6473794") "pn." or "20020200036"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/18 14:46
S50	4	("6463584" "6473794").pn. or "20020200036"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/18 14:47
S51	0	"20020200036"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/18 14:48
S52	3	"20020100036"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/18 14:48
S53	8	"binary translation" and (optimiz\$5 or optimis\$5 or optimal\$2) and ((record\$3 or journal\$4 or log\$4) near5 state) and (recover\$3 near5 state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/20 17:32

S54	9	"binary translation" and (optimiz\$5 or optimis\$5 or optimal\$2) and ((record\$3 or journal\$4 or log\$4 or stor\$3) near5 ((operation or function or method or procedure) and address)) and (recover\$3 near5 state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/21 08:10
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binary translation recovery documentation OR

1990

- 2001

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S Pereira, JO Ruanaidh, F Deguillaume, G Csurka, T ... - ICMCS, Vol. 1, 1999 - doi.ieeeecs.org

... the development of an original algorithm for the **recovery** of rotation, scaling and **translation** from a ... Let the message be represented in **binary** form as b ...

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Recovery of jump table case statements from **binary** code

C Cifuentes, MV Emmerik - Science of Computer Programming, 2001 - ieeexplore.ieee.org

Page 1. **Recovery** of Jump Table Case Statements from **Binary** Code & Cristina

Cifuentes Mike Van Emmerik Department of Computer Science ...

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Advances and future challenges in **binary translation** and optimization

ER Altman, K Ebcioglu, M Gschwind, S Sathaye - Proceedings of the IEEE, 2001 - ieeexplore.ieee.org

... al.: ADVANCES AND FUTURE CHALLENGES IN **BINARY TRANSLATION** AND **OPTIMIZATION** ... to theVMM and enters **recovery** code. ... Details, **documentation**, and code can be found at ...

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An out-of-order execution technique for runtime **binary** translators

BC Le - ACM SIGOPS Operating Systems Review, 1998 - portal.acm.org

... The rest of the paper is organized as follows. Section 2 discusses related work in the area of **binary translation** and exception **recovery**. ...

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Performance characterization of a hardware mechanism for dynamic optimization

B Fahs, S Bose, M Crum, B Slechta, F Spadini, T ... - Proceedings of the 34th annual ACM/IEEE international ..., 2001 - portal.acm.org

... optimizations without requiring **recovery** code. We ... Dynamic and Transparent **Binary Translation**, Computer, v.33 n.3, p ... conference on Systems **documentation** Douglas A ...

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Login. Search: The ACM Digital Library The Guide. ...

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A dynamic **binary translation** approach to architectural simulation

HW Cain, KM Lepak, MH Lipasti - ACM SIGARCH Computer Architecture News, 2001 - portal.acm.org

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K Ebcioglu, ER Altman, M Gschwind, S Sathaye - Proceedings of the 32nd annual ACM/IEEE international ..., 1999 - [portal.acm.org](#)

... optimizations in a dynamic **binary translation** system capable ... FX!32: A Profile-Directed **Binary** Translator, IEEE ... Run-time Detection and **Recovery** from Incorrectly ...

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[PS] The Design of a Resourceable and Retargetable **Binary** Translator

C Cifuentes, MV Emmerik, N Ramsey - WCRE, 1999 - [cs.virginia.edu](#)

... **Binary translation**, the automatic **translation** of exe- cutable programs from one machine to another, requires analyses and transformations that could be used in ...

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A robust foundation for **binary translation** of X 86 code

L Hsu - 1997 - [historical.ncstrl.org](#)

... Tandem denied the points for entry to and **recovery** from interpreter mode. There is no clear description about how the ... Background 3.1 **Binary Translation Model** ...

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Assembly to high-level language translation

C Cifuentes, D Simon, A Fraboulet - ICSM, 1998 - [doi.ieeeecs.org](#)

... **Translation** of assembly to HLL code requires a se ... an in- termediate representation suitable for HLL **recovery** analysis. ... has been used in RISC **binary** prolers like ...

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The Impact of Copyright on the Development of Cutting Edge **Binary** Reverse Engineering Technology

C Cifuentes - WCRE, 1999 - [ieeexplore.ieee.org](#)

... systems - Aid in **documentation** and debugging of ... Decompilation Decompilation Deconipilation **Binary translation** 1990s Emulation ... generated code - **Recovery** of lost ...

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K Ebcioglu, ER Altman, S Sathaye, M Gschwind - PROC ANNU INT SYMP MICROARCHITECTURE. pp. 284-295. 1999, 1999 - [ieeexplore.ieee.org](#)

... We describe several optimizations which can be em- ployed in a dynamic **binary translation** (DBT) system, where low compilation/**translation** overhead is essential ...

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A LINEAR METHOD FOR THE **RECOVERY** OF FUNCTIONS BASED ON **BINARY** DATA COMPLETION

AA Ligon, AA Shumeiko - Ukrainian Mathematical **Journal**, 2001 - [springerlink.com](#)

... Apparently, a method based on **binary** data completion was first considered ... off error ϵ , the error of the **recovery** of a ... m, where $f(0, v)$ is a **translation** of the ...

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Maximum separation in binary and multicomponent flash operations

DD Joye - *AIChE Journal*, 1993 - [doi.wiley.com](#)

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D Ung, C Cifuentes - 7 th Conference on Reverse Engineering(WCRE-2000), 2000 - [doi.ieeecomputersociety.org](#)

... view of the UQBT (University of Queensland **Binary** Translator) static **translation** framework [14,19 ... Similar to the idea of design **recovery**, the reverse ...

[Web Search](#) - [doi.ieeecs.org](#) - [ieeexplore.ieee.org](#) - [portal.acm.org](#) - all 5 versions »

The legal status of reverse engineering of computer software

C Cifuentes, A Fitzgerald - *Annals of Software Engineering*, 2000 - [springerlink.com](#)

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EM Petriu, JS Basran, FCA Groen, I INTRODUCTION - *IEEE Transactions on Instrumentation and Measurement*, 1990 - [ieeexplore.ieee.org](#)

... PETRIU et al. VEHICLE POSITION **RECOVERY** 255 celeration independent. ... III, OPriMiZING CODE CONVERSION A **translation** from the pseudorandom **binary** code into ...

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[PS] Re-engineering needs generic programming language technology

MJ van den Brand, P Klint, C Verhoef - *ACM SIGPLAN Notices*, 1997 - [science.uva.nl](#)

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Translation-invariant de-noising

RR Coifman, DL Donoho - *Wavelets and Statistics*, 1995 - [csee.wvu.edu](#)

... in frequency domain de-noising, where the goal of **translation**-invariance is ... no pseudo-Gibbs oscillations; a discontinuity near a **binary** irrational like $n/3$...

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1 [Legal deposit of digital publications: a review of research and development activity](#)

☒ Adrienne Muir

☒ January 2001 **Proceedings of the 1st ACM/IEEE-CS joint conference on Digital libraries**
Publisher: ACM Press
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There is a global trend towards extending legal deposit to include digital publications in order to maintain comprehensive national archives. However, including digital publications in legal deposit regulation is not enough to ensure the long-term preservation of these publications. Concepts, principles and practices accepted and understood in the print environment, may have new meanings or no longer be appropriate in a networked environment. Mechanisms for identifying, selecting and depositing ...

Keywords: digital preservation, digital publications, legal deposit

2 [Re-engineering needs generic programming language technology](#)

☒ Mark van den Brand, Paul Klint, Chris Verhoef


☒ February 1997 **ACM SIGPLAN Notices**, Volume 32 Issue 2
Publisher: ACM Press
 Full text available: ☒ [pdf\(712.76 KB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

Generic language technology and compiler construction techniques are a prerequisite to build analysis and conversion tools that are needed for the re-engineering of large software systems. We argue that generic language technology is a crucial means to do fundamental re-engineering. Furthermore, we address the issue that the application of compiler construction techniques in re-engineering generates new research questions in the field of compiler construction.

Keywords: compiler construction techniques, generic language technology, intermediate data representation, programming environment generator, re-engineering, reverse engineering, system renovation

3 Fast detection of communication patterns in distributed executions


Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research****Publisher:** IBM PressFull text available:  pdf(4.21 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

4 Technique for automatically correcting words in text

Karen Kukich

December 1992 **ACM Computing Surveys (CSUR)**, Volume 24 Issue 4**Publisher:** ACM PressFull text available:  pdf(6.23 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Research aimed at correcting words in text has focused on three progressively more difficult problems: (1) nonword error detection; (2) isolated-word error correction; and (3) context-dependent word correction. In response to the first problem, efficient pattern-matching and n-gram analysis techniques have been developed for detecting strings that do not appear in a given word list. In response to the second problem, a variety of general and application-specific spelling cor ...


Keywords: n-gram analysis, Optical Character Recognition (OCR), context-dependent spelling correction, grammar checking, natural-language-processing models, neural net classifiers, spell checking, spelling error detection, spelling error patterns, statistical-language models, word recognition and correction

5 MPEG-4: an object-based multimedia coding standard supporting mobile applications


Atul Puri, Alexandros Eleftheriadis

June 1998 **Mobile Networks and Applications**, Volume 3 Issue 1**Publisher:** Kluwer Academic PublishersFull text available:  pdf(747.80 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

The ISO MPEG committee, after successful completion of the MPEG-1 and the MPEG-2 standards is currently working on MPEG-4, the third MPEG standard. Originally, MPEG-4 was conceived to be a standard for coding of limited complexity audio-visual scenes at very low bit-rates; however, in July 1994, its scope was expanded to include coding of scenes as a collection of individual audio-visual objects and enabling a range of advanced functionalities not supported by other standards. One of the ke ...

- 6 IS '97: model curriculum and guidelines for undergraduate degree programs in information systems
 Gordon B. Davis, John T. Gorgone, J. Daniel Couger, David L. Feinstein, Herbert E. Longenecker
 December 1996 **ACM SIGMIS Database , Guidelines for undergraduate degree programs on**
Model curriculum and guidelines for undergraduate degree programs in
information systems IS '97, Volume 28 Issue 1

Publisher: ACM Press


Full text available:  pdf(7.24
 MB)

Additional Information: [full citation](#), [citations](#)

- 7 Motion recovery for video content classification

 Nevenka Dimitrova, Forouzan Golshani
 October 1995 **ACM Transactions on Information Systems (TOIS)**, Volume 13 Issue 4

Publisher: ACM Press

Full text available:  pdf(2.74
 MB)

Additional Information: [full citation](#), [abstract](#), [references](#),
[citations](#), [index terms](#)

Like other types of digital information, video sequences must be classified based on the semantics of their contents. A more-precise and completer extraction of semantic information will result in a more-effective classification. The most-discernible difference between still images and moving pictures stems from movements and variations. Thus, to go from the realm of still-image repositories to video databases, we must be able to deal with motion. Particularly, we need the ability to classi ...


Keywords: MPEG compressed video analysis, content-based retrieval of video, motion recovery, video databases, video retrieval

- 8 Asymptotically good codes correcting insertions, deletions, and transpositions

Leonard J. Schulman, David Zuckerman

January 1997 **Proceedings of the eighth annual ACM-SIAM symposium on Discrete algorithms**

Publisher: Society for Industrial and Applied Mathematics

Full text available:  pdf(689.23
 KB)


Additional Information: [full citation](#), [references](#), [index terms](#)

- 9 Schema translation using structural transformation

Rateb Abu-Hamdeh, James R. Cordy, Patrick Martin

October 1994 **Proceedings of the 1994 conference of the Centre for Advanced Studies on Collaborative research**

Publisher: IBM Press


Full text available:  pdf(196.83
 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)





This paper describes how structural transformation can be applied to the problem of translating schemas expressed in one data model into equivalent schemas expressed in another data model. We explain our approach to the problem which involves translating a schema in the source data model into a set of facts in a knowledge base and from there into a schema in the target data model. We present an example transformation in detail and outline how one can analyze the information capacity preserving p ...

10 HyperFile: a data and query model for documents

Chris Clifton, Hector Garcia-Molina, David Bloom


January 1995 **The VLDB Journal — The International Journal on Very Large Data Bases**,
Volume 4 Issue 1**Publisher:** Springer-Verlag New York, Inc.Full text available:  [pdf\(2.04 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Non-quantitative information such as documents and pictures pose interesting new problems in the database world. Traditional data models and query languages do not provide appropriate support for this information. Such data are typically stored in file systems, which do not provide the security, integrity, or query features of database management systems. The hypertext model has emerged as a good interface to this information; however, *finding* information using hypertext browsing does not ...









Keywords: hypertext, indexing, user interface**11** Towards an Ada basis for KBSE: Refine-Ada 95 conversion Paul A. Bailes, Paul Burnim, Murray Chapman, Eric SalzmanDecember 1996 **Proceedings of the conference on TRI-Ada '96: disciplined software development with Ada****Publisher:** ACM PressFull text available:  [pdf\(1.01 MB\)](#)Additional Information: [full citation](#), [references](#), [index terms](#)**12** The use of description logics in KBSE systems Premkumar Devanbu, Mark A. JonesApril 1997 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 6 Issue 2**Publisher:** ACM PressFull text available:  [pdf\(365.07 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The increasing size and complexity of many software systems demand a greater emphasis on capturing and maintaining knowledge at many different levels within the software development process. This knowledge includes descriptions of the hardware and software components and their behavior, external and internal design specifications, and support for system testing. The Knowledge-based software engineering (KBSE) research paradigm is concerned with systems that use formally represented knowledge ...



Keywords: automated software engineering, knowledge basis, logics, software development environments, testing, tools**13** Algorithm 719: Multiprecision translation and execution of FORTRAN programs David H. BaileySeptember 1993 **ACM Transactions on Mathematical Software (TOMS)**, Volume 19 Issue 3

Publisher: ACM PressFull text available:  [pdf\(2.03 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


This paper describes two Fortran utilities for multiprecision computation. The first is a package of Fortran subroutines that perform a variety of arithmetic operations and transcendental functions on floating point numbers of arbitrarily high precision. This package is in some cases over 200 times faster than that of certain other packages that have been developed for this purpose. The second utility is a translator program, which facilitates the conversion of ordinary Fortran p ...

Keywords: multiple-precision computation, multiprecision arithmetic**14** [Translating description logics to information server queries](#) Premkumar T. Devanbu December 1993 **Proceedings of the second international conference on Information and knowledge management****Publisher:** ACM PressFull text available:  [pdf\(969.83 KB\)](#)Additional Information: [full citation](#), [references](#), [index terms](#)**15** [Incremental cryptography and application to virus protection](#) Mihir Bellare, Oded Goldreich, Shafi Goldwasser May 1995 **Proceedings of the twenty-seventh annual ACM symposium on Theory of computing****Publisher:** ACM PressFull text available:  [pdf\(1.65 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**16** [AdaPT and Ada 9X](#) S. J. Goldsack, A. A. Holzbacher-Valero, R. Volz, R. Waldrop March 1994 **ACM SIGAda Ada Letters**, Volume XIV Issue 2**Publisher:** ACM PressFull text available:  [pdf\(1.18 MB\)](#)Additional Information: [full citation](#), [abstract](#), [index terms](#)

Following an initial proposal developed at the 4th Ada Real Time Programming Workshop, Nemaquin Woodlands, 1989, and further meetings to pursue the ideas, a series of papers were published [1][2][3] describing and discussing possible Ada language extensions to support the development of distributable programs. The language consisting of Ada with these extensions has been called AdaPT, which may be thought of as Ada with partitions. After introducing AdaPT, this paper describes how the AdaPT con ...

17 [Indexing handwriting using word matching](#) R. Manmatha, Chengfeng Han, E. M. Riseman, W. B. Croft April 1996 **Proceedings of the first ACM international conference on Digital libraries**


Publisher: ACM Press

Full text available:  [pdf\(947.23 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

18 [Multimedia document architecture \(panel session\)](#)

 Stephen Bulick, Terry Crowley, Lester Ludwig, Jonathan Rosenberg
August 1990 **ACM SIGGRAPH 90 Panel Proceedings**

Publisher: ACM Press

Full text available:  [pdf\(4.35 MB\)](#) Additional Information: [full citation](#), [index terms](#)

19 [Distributed systems - programming and management: On remote procedure call](#)

Patrícia Gomes Soares

November 1992 **Proceedings of the 1992 conference of the Centre for Advanced Studies on Collaborative research - Volume 2**

Publisher: IBM Press

Full text available:  [pdf\(4.52 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

The Remote Procedure Call (RPC) paradigm is reviewed. The concept is described, along with the backbone structure of the mechanisms that support it. An overview of works in supporting these mechanisms is discussed. Extensions to the paradigm that have been proposed to enlarge its suitability, are studied. The main contributions of this paper are a standard view and classification of RPC mechanisms according to different perspectives, and a snapshot of the paradigm in use today and of goals for t ...

20 [InterViso: dealing with the complexity of federated database access](#)

Marjorie Templeton, Herbert Henley, Edward Maros, Darrel J. Van Buer

April 1995 **The VLDB Journal — The International Journal on Very Large Data Bases**, Volume 4 Issue 2

Publisher: Springer-Verlag New York, Inc.

Full text available:  [pdf\(1.87 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Connectivity products are finally available to provide the "highways" between computers containing data. IBM has provided strong validation of the concept with their "Information Warehouse." DBMS vendors are providing gateways into their products, and SQL is being retrofitted on many older DBMSs to make it easier to access data from standard 4GL products and application development systems. The next step needed for data integration is to provide (1) a common data dictionary with a conceptual sch ...

Keywords: data warehouse, database integration, federated database

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Relevance scale ☐ ☐ ☐**1** [Optimization and precise exceptions in dynamic compilation](#)

Michael Gschwind, Erik Altman

March 2001 **ACM SIGARCH Computer Architecture News**, Volume 29 Issue 1**Publisher:** ACM PressFull text available: [pdf\(508.52 KB\)](#)Additional Information: [full citation](#), [abstract](#), [index terms](#)

Maintaining precise exceptions is an important aspect of achieving full compatibility with a legacy architecture. While asynchronous exceptions can be deferred to an appropriate boundary in the code, synchronous exceptions must be taken when they occur. This introduces uncertainty into liveness analysis since processor state that is otherwise dead may be exposed when an exception handler is invoked. Previous systems either had to sacrifice full compatibility to achieve more freedom to perform op ...

2 [Binary translation](#)

Richard L. Sites, Anton Chernoff, Matthew B. Kirk, Maurice P. Marks, Scott G. Robinson


February 1993 **Communications of the ACM**, Volume 36 Issue 2**Publisher:** ACM PressFull text available: [pdf\(4.84 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: CISC computers, RISC computers, binary translation, computer architecture, processor architecture translation

3 [Dynamic translation: The Transmeta Code Morphing™ Software: using speculation, recovery, and adaptive retranslation to address real-life challenges](#)

James C. Dehnert, Brian K. Grant, John P. Banning, Richard Johnson, Thomas Kistler, Alexander Klaiber, Jim Mattson


March 2003 **Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization CGO '03****Publisher:** IEEE Computer Society


Full text available:  [pdf\(988.25 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Transmeta's Crusoe microprocessor is a full, system-level implementation of the x86 architecture, comprising a native VLIW microprocessor with a software layer, the **Code Morphing Software** (CMS), that combines an interpreter, dynamic binary translator, optimizer, and runtime system. In its general structure, CMS resembles other binary translation systems described in the literature, but it is unique in several respects. The wide range of PC workloads that CMS must handle gracefully in real ...

Keywords: binary translation, dynamic optimization, dynamic translation, emulation, self-modifying code, speculation

4 [Binary translation and architecture convergence issues for IBM system/390](#)



 Michael Gschwind, Kemal Ebcioglu, Erik Altman, Sumedh Sathaye
May 2000 **Proceedings of the 14th international conference on Supercomputing**
Publisher: ACM Press

Full text available:  [pdf\(1.44 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We describe the design issues in an implementation of the ESA/390 architecture based on binary translation to a very long instruction word (VLIW) processor. During binary translation, complex ESA/390 instructions are decomposed into instruction "primitives" which are then scheduled onto a wide-issue machine. The aim is to achieve high instruction level parallelism due to the increased scheduling and optimization opportunities which can be exploited by binary translation software ...


5 [Optimizations and oracle parallelism with dynamic translation](#)


Kemal Ebcioglu, Erik R. Altman, Michael Gschwind, Sumedh Sathaye
November 1999 **Proceedings of the 32nd annual ACM/IEEE international symposium on Microarchitecture**
Publisher: IEEE Computer Society

Full text available:  [pdf\(1.28 MB\)](#)  Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)
[Publisher Site](#)

We describe several optimizations which can be employed in a dynamic binary translation (DBT) system, where low compilation/translation overhead is essential. These optimizations achieve a high degree of ILP, sometimes even surpassing a static compiler employing more sophisticated, and more time-consuming algorithms [9]. We present results in which we employ these optimizations in a dynamic binary translation system capable of computing oracle parallelism.

6 [An out-of-order execution technique for runtime binary translators](#)

 Bich C. Le
October 1998 **ACM SIGOPS Operating Systems Review**, **ACM SIGPLAN Notices**, **Proceedings of the eighth international conference on Architectural support for programming languages and operating systems ASPLOS-VIII**, Volume 32, 33 Issue 5, 11
Publisher: ACM Press


Full text available:  [pdf\(1.04 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A dynamic translator emulates an instruction set architecture by translating source instructions to native code during execution. On statically-scheduled hardware, higher performance can potentially be achieved by reordering the translated instructions; however, this is a challenging transformation if the source architecture supports precise exception semantics, and the user-level program is allowed to register exception handlers. This paper presents a software technique which allows a translator ...

7 Going native: Module-aware translation for real-life desktop applications

 Jianhui Li, Peng Zhang, Orna Etzion
June 2005 **Proceedings of the 1st ACM/USENIX international conference on Virtual execution environments**


Publisher: ACM Press

Full text available:  [pdf\(584.15 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)


A dynamic binary translator is a just-in-time compiler that translates source architecture binaries into target architecture binaries on the fly. It enables the fast running of the source architecture binaries on the target architecture. Traditional dynamic binary translators invalidate their translations when a module is unloaded, so later re-loading of the same module will lead to a full retranslation. Moreover, most of the loading and unloading are performed on a few "hot" modules, which causes ...

Keywords: dynamic binary translation, dynamic loaded module, memory management, translation reuse

8 Continuous program optimization: A case study

 Thomas Kistler, Michael Franz
July 2003 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 25 Issue 4


Publisher: ACM Press

Full text available:  [pdf\(877.67 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [review](#)


Much of the software in everyday operation is not making optimal use of the hardware on which it actually runs. Among the reasons for this discrepancy are hardware/software mismatches, modularization overheads introduced by software engineering considerations, and the inability of systems to adapt to users' behaviors. A solution to these problems is to delay code generation until load time. This is the earliest point at which a piece of software can be fine-tuned to the actual capabilities of the ...

Keywords: Dynamic code generation, continuous program optimization, dynamic reoptimization

9 A precision- and range-independent tool for testing floating-point arithmetic I: basic operations, square root, and remainder

 Bridgitte Verdonk, Annie Cuyt, Dennis Verschaeren

March 2001 **ACM Transactions on Mathematical Software (TOMS)**, Volume 27 Issue 1
Publisher: ACM Press

Full text available:  [pdf\(167.76 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


This paper introduces a precision- and range-independent tool for testing the compliance of hardware or software implementations of (multiprecision) floating-point arithmetic with the principles of the IEEE standards 754 and 854. The tool consists of a driver program, offering many options to test only specific aspects of the IEEE standards, and a large set of test vectors, encoded in a precision-independent syntax to allow the testing of basic and extended hardware formats as well as multi ...

Keywords: IEEE floating-point standard, arithmetic, floating-point, multiprecision, validation

- 10 [A precision- and range-independent tool for testing floating-point arithmetic II: conversions](#)
 Brigitte Verdonk, Annie Cuyt, Dennis Verschaeren



March 2001 **ACM Transactions on Mathematical Software (TOMS)**, Volume 27 Issue 1
Publisher: ACM Press

Full text available:  [pdf\(125.98 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The IEEE 754 and 854 standards for floating-point arithmetic are essentially a specification of a programming environment, encompassing aspects from computer hardware, operating systems, and compilers to programming languages (see especially Section 8). Parts I and II of this paper together describe a tool to test floating-point implementations of arbitrary precision and exponent range (hardware as well as software) for compliance with the principles outlined in the IEEE standards. The tool ...

Keywords: IEEE floating-point standard, arithmetic, conversion, decimal, floating-point, multiprecision, validation

- 11 [RABIT: A New Framework for Runtime Emulation and Binary Translation](#)

Suranjan Pramanik, Shambhu J. Upadhyaya

April 2004 **Proceedings of the 37th annual symposium on Simulation ANSS '04**

Publisher: IEEE Computer Society

Full text available:  [pdf\(282.95 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

Binary translation has been widely used as object codemigration across different architectures. Most currentworks are targeted towards running an existing (old architecture)binary version of a complex application on a newerarchitecture and so availability of resources is not a problem.In this paper we propose a technique called RABIT foremulating a newer architecture's binary on an older one.RABIT will allow the consumers to emulate the applicationsdeveloped for newer hardware on their older mac ...


- 12 [DAISY: dynamic compilation for 100% architectural compatibility](#)



Kemal Ebcioglu, Erik R. Altman

May 1997 **ACM SIGARCH Computer Architecture News , Proceedings of the 24th annual international symposium on Computer architecture ISCA '97**, Volume 25 Issue 2

Publisher: ACM Press


Full text available:  pdf(1.97 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Although VLIW architectures offer the advantages of simplicity of design and high issue rates, a major impediment to their use is that they are not compatible with the existing software base. We describe new simple hardware features for a VLIW machine we call **DAISY** (**D**ynamically **A**rchitected **I**nstruction **S**et from **Y**orktown). **DAISY** is specifically intended to emulate existing architectures, so that all existing softwa ...


Keywords: binary translation, dynamic compilation, instruction-level parallelism, object code compatible VLIW, superscalar

13 [A dynamic binary translation approach to architectural simulation](#)

 Harold W. Cain, Kevin M. Lepak, Mikko H. Lipasti

March 2001 **ACM SIGARCH Computer Architecture News**, Volume 29 Issue 1


Publisher: ACM Press

Full text available:  pdf(669.63 KB)

Additional Information: [full citation](#), [abstract](#), [index terms](#)


We present the design of a PowerPC-based simulation infrastructure for architectural research. Our infrastructure uses an execution-driven out-of-order processor timing simulator from the SimpleScalar tool set. While porting SimpleScalar to the PowerPC architecture, we would like to remain compatible with other versions of SimpleScalar. We accomplish this by performing dynamic binary translation of the PowerPC instruction set architecture to the SimpleScalar instruction set architecture, and by ...

14 [An architectural framework for migration from CISC to higher performance platforms](#)

 Gabriel M. Silberman, Kemal Ebcioglu

August 1992 **Proceedings of the 6th international conference on Supercomputing**


Publisher: ACM Press

Full text available:  pdf(2.04 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


We describe a novel architectural framework that allows software applications written for a given Complex Instruction Set Computer (CISC) to migrate to a different, higher performance architecture, without a significant investment on the part of the application user or developer. The framework provides a hardware mechanism for seamless switching between two instruction sets, resulting in a machine that enhances application performance while keeping the same program behavior (from a user per ...

15 [A proposed standard for binary floating point arithmetic](#)

 Jerome Coonen, William Kahan, John Palmer, Tom Pittman, David Stevenson

October 1979 **ACM SIGNUM Newsletter**, Volume 14 Issue si-2

Publisher: ACM Press

Full text available:  pdf(643.23 KB)

Additional Information: [full citation](#), [abstract](#)

This standard is a product of the Floating Point Working Group of the Microprocessor Standards Subcommittee of the IEEE Computer Society Computer Standards Committee. It is intended that the standard embody the essence of "Specifications For a Proposed Standard for Floating Point


Arithmetic" by Jerome Coonen.


16 Incremental Commit Groups for Non-Atomic Trace Processing

Matt T. Yourst, Kanad Ghose

November 2005 **Proceedings of the 38th annual IEEE/ACM International Symposium on Microarchitecture MICRO 38**

Publisher: IEEE Computer Society

Full text available:  [pdf\(614.37](#)

[KB\)](#) 

[Publisher Site](#)

Additional Information: [full citation](#), [abstract](#)

We introduce techniques to support efficient non-atomic execution of very long traces on a new binary translation based, x86-64 compatible VLIW microprocessor. Incrementally committed long traces significantly reduce wasted computations on exception induced rollbacks by retaining the correctly committed parts of traces. We divide each scheduled trace into multiple commit groups; groups are committed to the architectural state after all instructions within and prior to each group complete without ...

Keywords: binary translation, VLIW, commitment, trace prediction


17 On the need for predictable floating-point arithmetic in the programming languages Fortran 90 and C/C++



Dennis Verschaeren, Annie Cuyt, Brigitte Verdonk

March 1997 **ACM SIGPLAN Notices**, Volume 32 Issue 3

Publisher: ACM Press

Full text available:  [pdf\(870.21](#)

[KB\)](#)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

During the past decade the IEEE 754 standard for binary floating-point arithmetic has been very successful. Many of today's hardware platforms conform to this standard and compilers should make floating-point functionality available to programmers through high-level programming languages. They should also implement certain features of the IEEE 754 standard in software, such as the input and output conversions to and from the internal binary representation of numbers. In this report, a number of ...


Keywords: C, C++, Fortran 90, IEEE floating-point standard

18 Dynamic translation: Dynamic binary translation for accumulator-oriented architectures

Ho-Seop Kim, James E. Smith

March 2003 **Proceedings of the international symposium on Code generation and optimization: feedback-directed and runtime optimization CGO '03**

Publisher: IEEE Computer Society

Full text available:  [pdf\(1.13](#)

[MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


A dynamic binary translation system for a co-designed virtual machine is described and evaluated. The underlying hardware directly executes an accumulator-oriented instruction set that exposes instruction dependence chains (strands) to a distributed microarchitecture containing a simple instruction pipeline. To support conventional program binaries, a source

instruction set (Alpha in our study) is dynamically translated to the target accumulator instruction set. The binary translator identifies ...

19 [Proceedings of the SIGNUM conference on the programming environment for development of numerical software](#)

 March 1979 **ACM SIGNUM Newsletter**, Volume 14 Issue 1

Publisher: ACM Press

Full text available:  [pdf\(5.02 MB\)](#)


Additional Information: [full citation](#)

20 [Development and implementation of the Magnavox generic Ada basic mathematics package](#)

 K Rehmer

May 1987 **ACM SIGAda Ada Letters**, Volume VII Issue 3

Publisher: ACM Press

Full text available:  [pdf\(702.22 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

This paper explains development work on a mathematics library at Magnavox during the summer of 1985 and some revisions which were made in the early summer of 1986. Briefly, sources for a basic mathematics library will be explored, reasons behind some of the design decisions made will be discussed. Finally, a brief presentation of the method of testing will be given.

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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

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If an **exception** occurs when the data is needed, the **exception** will be recognized

... It is possible to run x86-code on Alpha using FX132 **binary translator**. ...

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Method for effective **binary translation** between different instruction sets ...

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... microprocessor with low energy consumption, **binary** compatibility with ... **translation**

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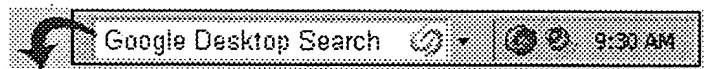
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